

ABSTRACT OF THE DISCLOSURE

A CPU controls the storage of isochronous packets into a main memory. The CPU inserts a nullpacket, which will be ignored by a reception apparatus, into isochronous packets so that the isochronous packets have the same size. A 1394 DMA controller controls the transmission of stored isochronous packets, which have the same size due to the inserted nullpacket, over a 1394 bus.